

Figure 1A

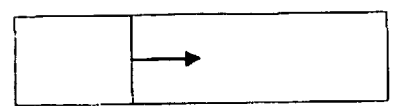
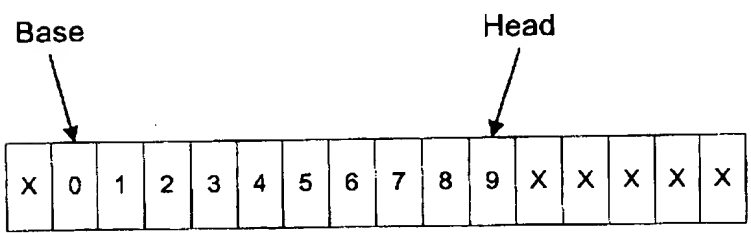


Figure 1B

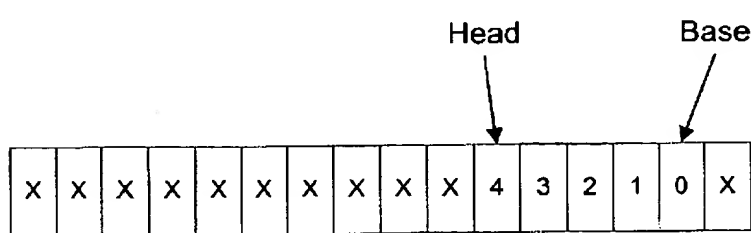


Figure 2A

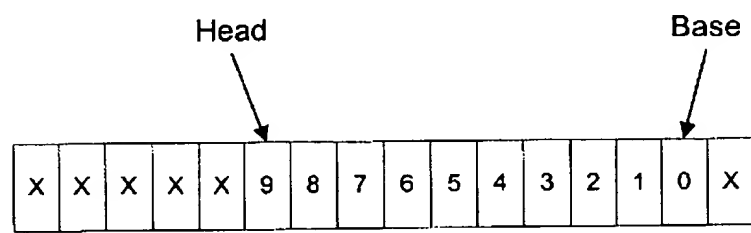
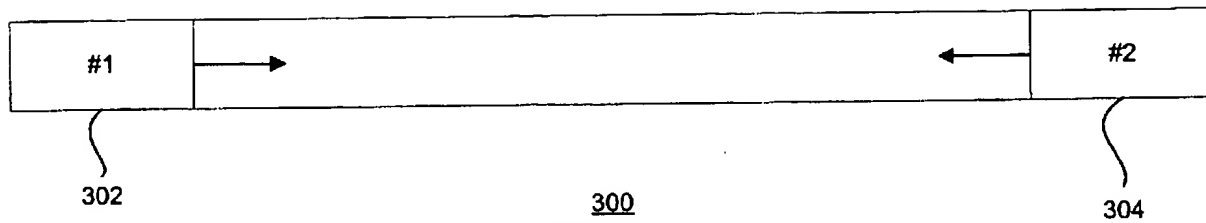
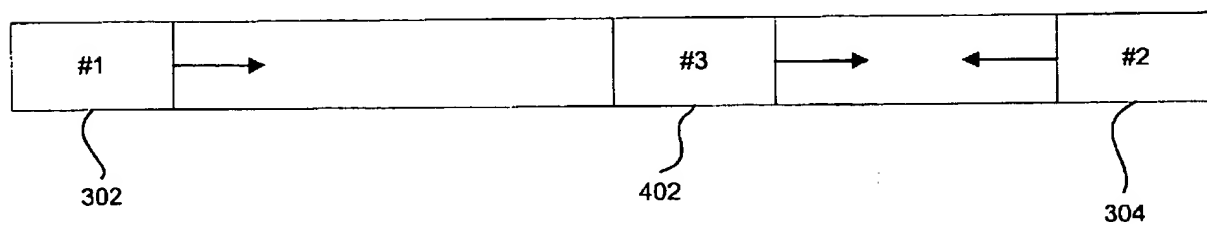


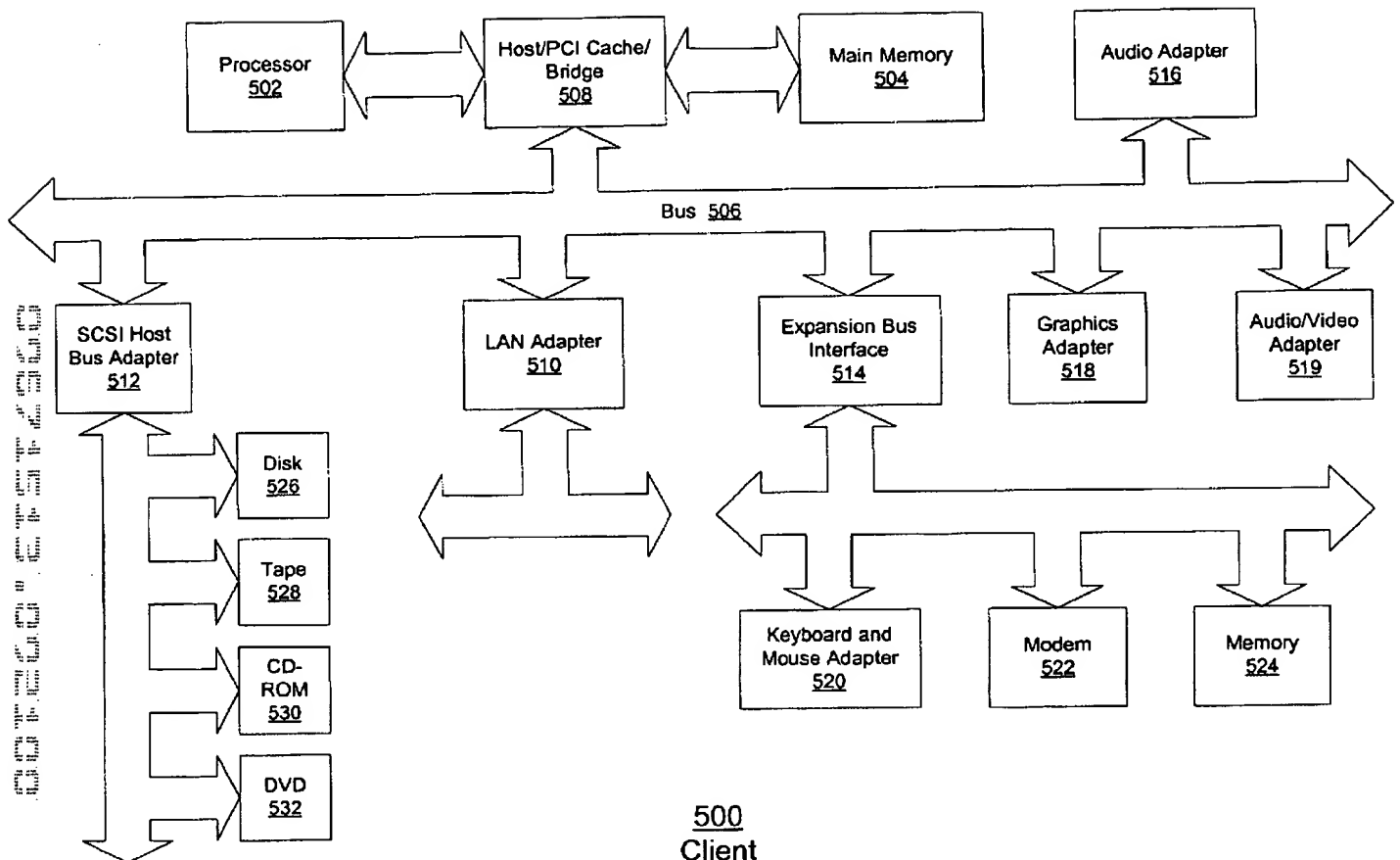
Figure 2B



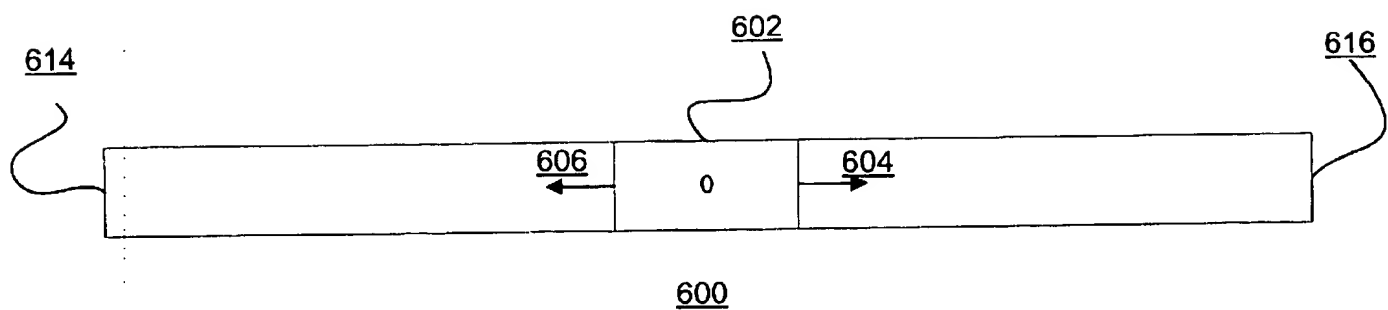
300
Memory Array
Figure 3



300
Memory Array
Figure 4

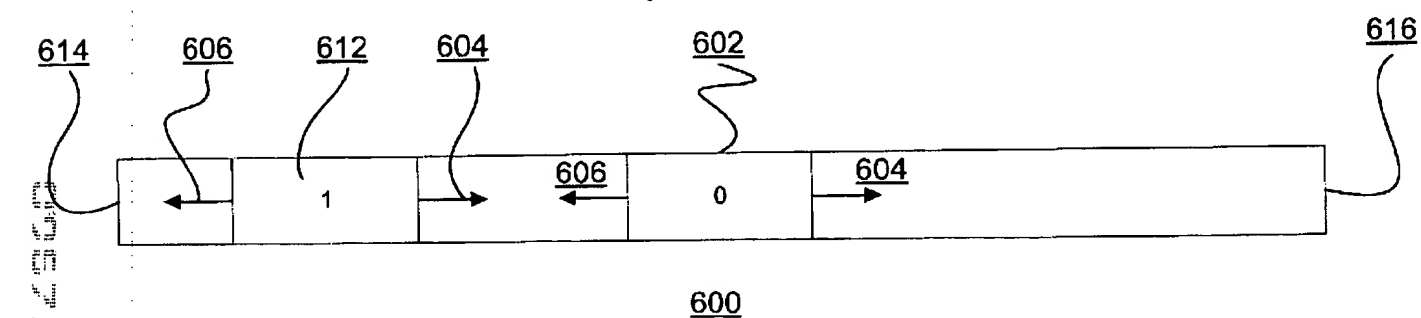


500
Client
Figure 5
AUS9-2000-0436-US1



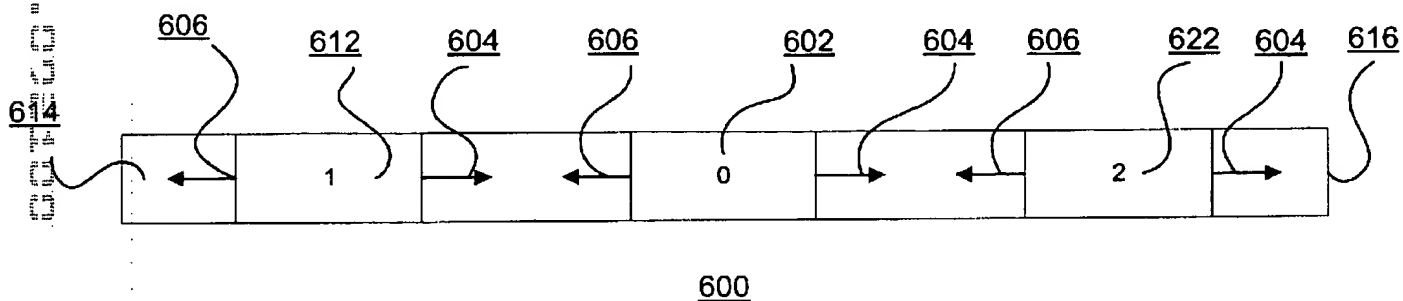
One Bi-Directional Stack

Figure 6A



Two Bi-Directional Stacks

Figure 6B



Three Bi-Directional Stacks

Figure 6C

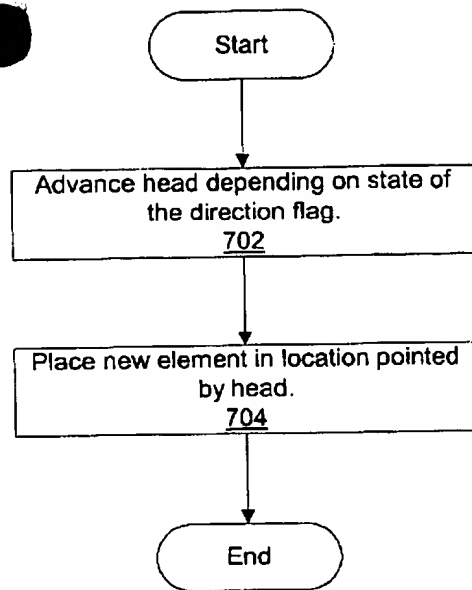


Figure 7A
(Prior Art)

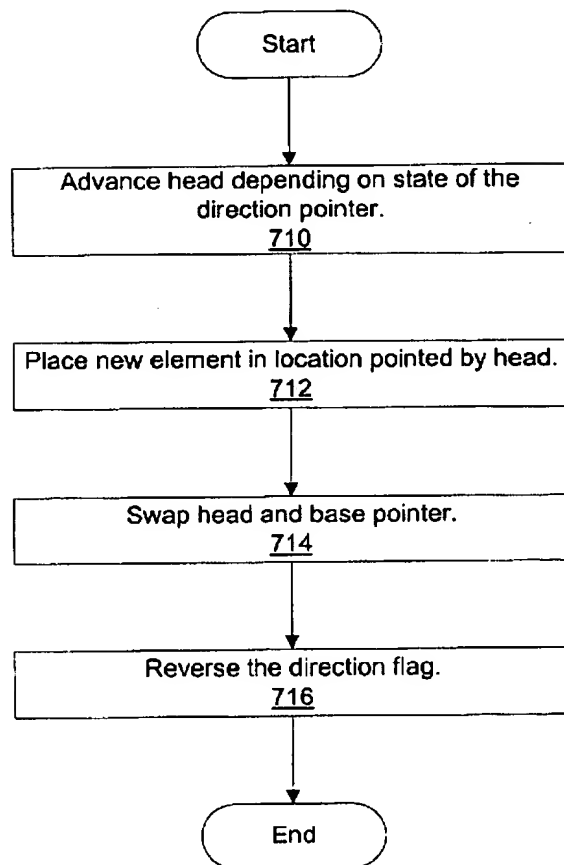


Figure 7B
AUS9-2000-0436-US1

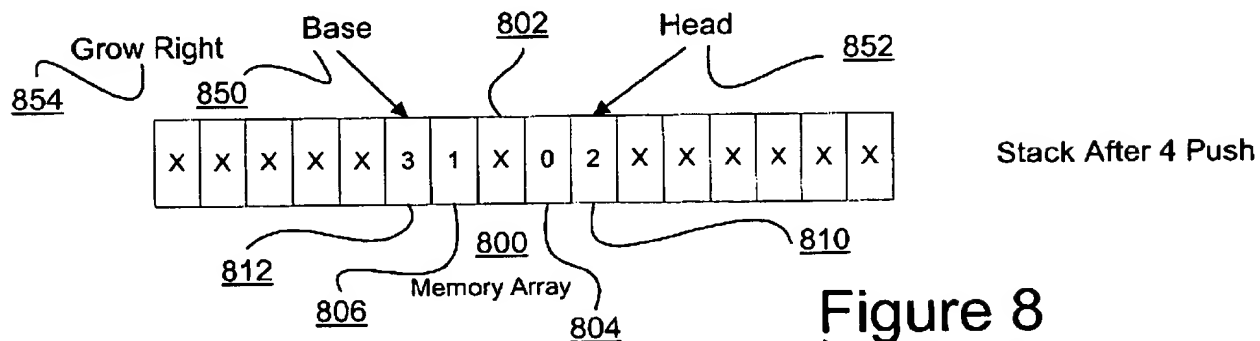
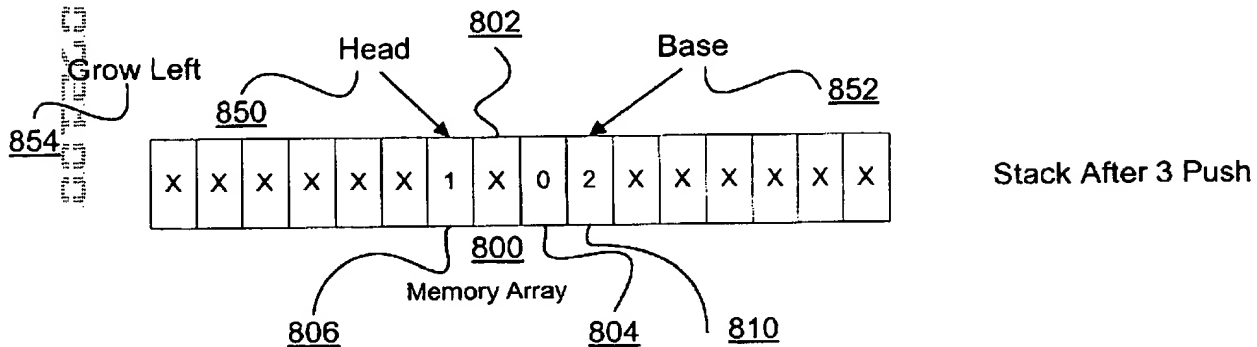
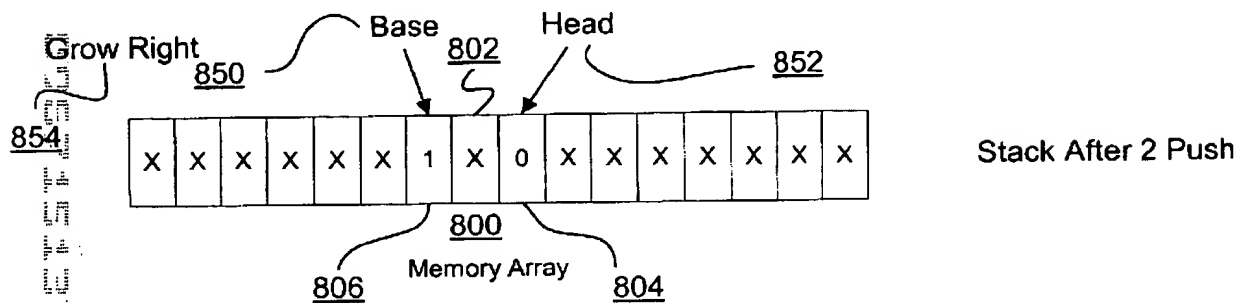
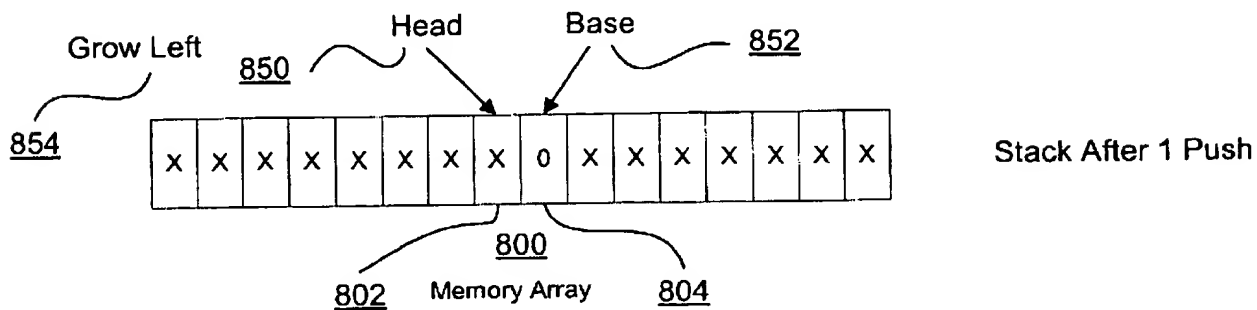
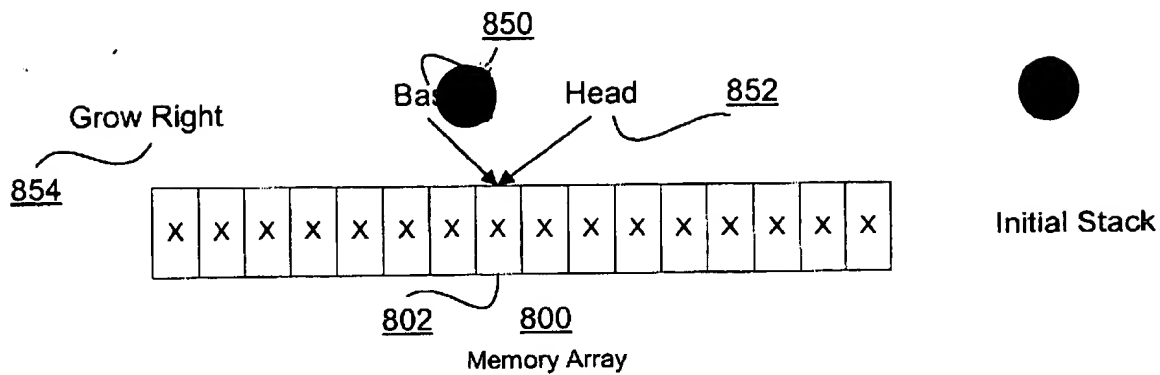


Figure 8
AUS9-2000-0436-US1

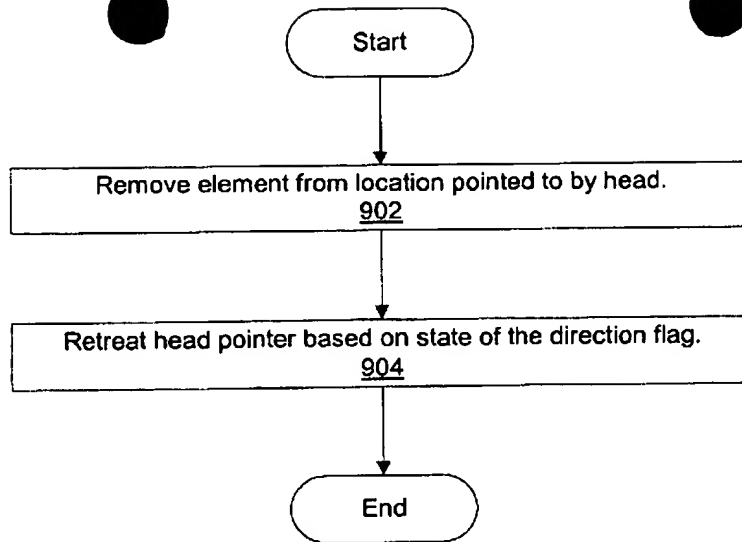


Figure 9A
(Prior Art)

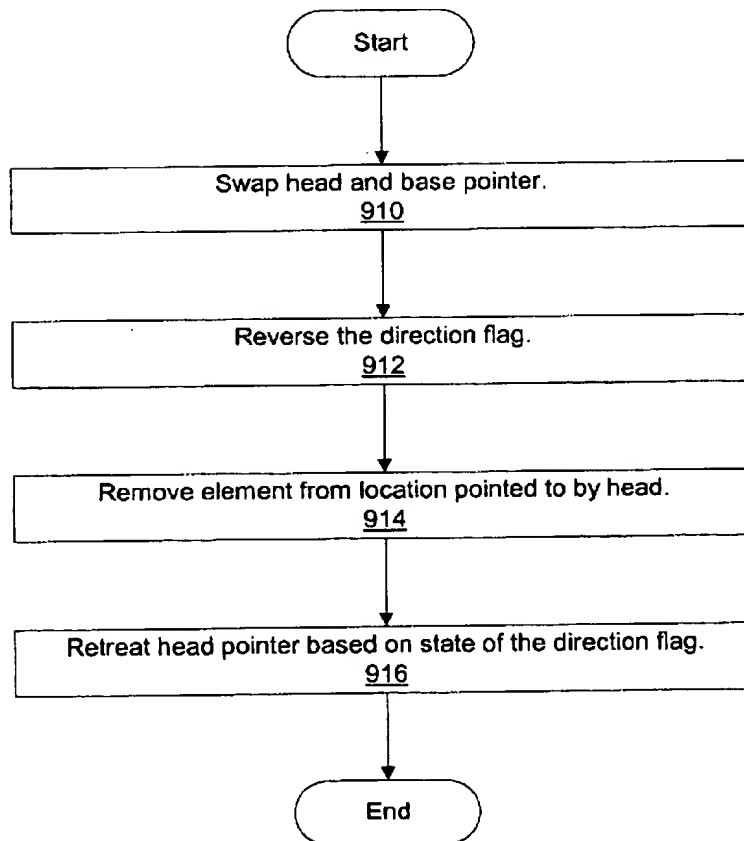


Figure 9B
AUS9-2000-0436-US1

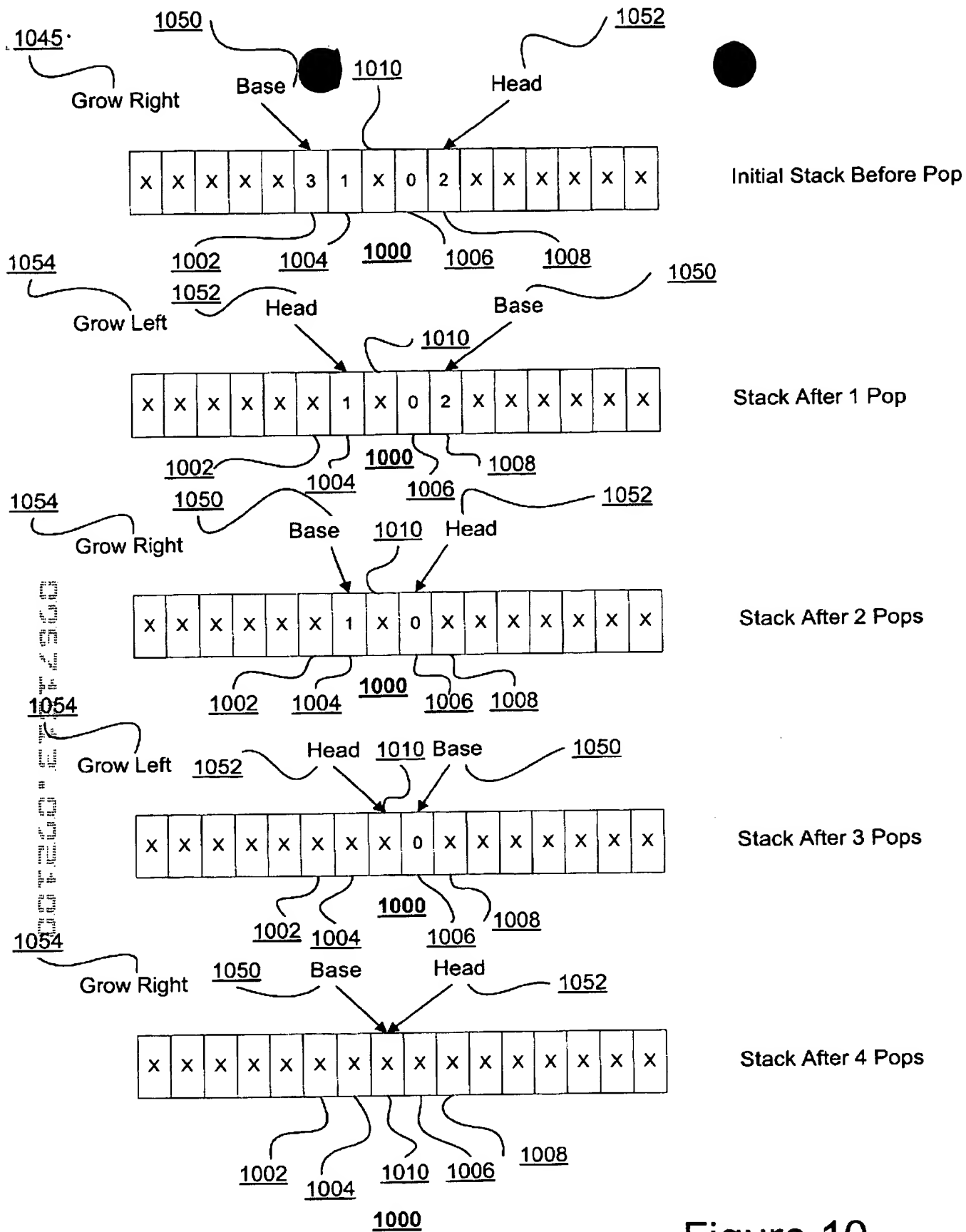


Figure 10

AUS9-2000-0436-US1